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Design of a Multiple-Input SC DC-DC Converter Realizing Long Battery Runtime

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SUMMARY A multiple-input switched-capacitor DC-DC converter which can realize long battery runtime is proposed in this letter. Unlike conventional converters for a back-lighting application, the proposed converter drives some LEDs by converting energy from solar cells. Furthermore, the proposed converter can charge a lithium battery when an output load is light. The validity of circuit design is confirmed by theoretical analyses, simulations, and experiments.

key words: DC-DC converters, switched-capacitor circuits, back-lighting applications, clean energy

2. Circuit Structure

2.1 Conventional Converter

Figure 1 shows an example of conventional converters for mobile back-lighting applications. The converter shown in Fig. 1 is one of the most popular converters and is adopted in MAX1910, MAX1912*, and so on. As shown in Fig. 1, power switches S1–S7 are driven by non-overlapped 2-phase clock pulses synchronously. Since the conversion ratio is predetermined by the circuit structure, the conventional converter is difficult to improve power efficiency by adjusting the conversion ratio.

2.2 Proposed Converter

Figure 2 shows a general form of the proposed converter. The proposed converter consists of 5N + 7 (N = 1, 2, …) power switches and N + 2 capacitors. As Fig. 2 shows, the proposed converter has two kinds of different inputs: V_m1 which is offered by a lithium battery and V_m2 which is supplied by solar cells. When solar cells offer enough electric power, the proposed converter drives LEDs by converting solar energy input V_m2. Furthermore, the lithium battery is charged when the output load is light. Only when the solar cells cannot generate enough electric power, the LEDs

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*MAX1910 and MAX1912 are 1.5 × /2× step-up converters produced by Maxim Integrated Products.

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are driven by converting the energy from the lithium battery. Hence, the proposed converter enables us to realize long battery runtime.

In the following section, to help readers’ understanding, theoretical analyses will be performed concerning the simplest converter shown in Fig. 3.

### 3. Theoretical Analysis

According to $V_{in2}$, the proposed converter shown in Fig. 3 offers 4 kinds of conversion modes. When $V_{in1}/2 \leq V_{in2} < 3V_{in1}/4$, $3V_{in1}/4 \leq V_{in2} < V_{in1}$, and $V_{in2} \leq V_{in1}$, the proposed converter realizes $3x$, $2x$, and $1.5x$ step-up conversion, respectively, where the input voltage is $V_{in2}$. On the other hand, the proposed converter realizes $1.5x$ step-up conversion when $V_{in2} < V_{in1}/2$, where the input voltage is $V_{in1}$. Figure 4 shows instantaneous equivalent circuits when $3x$ step-up mode.

First, the equivalent circuit when $3x$ step-up conversion is analyzed. In the theoretical analysis, we assume that 1. parasitic elements are negligibly small and 2. time constant is much larger than the period of clock pulses. In the steady state of Fig. 4, differential values of electric charges in $C_k$ ($k = 1, 2, 3$) satisfy

$$\Delta q^{1}_{T1} + \Delta q^{k}_{T2} = 0,$$  \hspace{1cm} (1)

where $\Delta q^{1}_{T1}$ and $\Delta q^{k}_{T2}$ denote electric charges when $State - T1$ and $State - T2$, respectively. In the case of $State - T1$, differential values of the electric charges in terminal-$I2$ and terminal-$O$, $\Delta q_{T1,V_o}$ and $\Delta q_{T1,V_i^o}$, are given by

$$\Delta q_{T1,V_o} = \Delta q^{1}_{T1} + \Delta q^{2}_{T1}$$

and

$$\Delta q_{T1,V_i^o} = \Delta q^{3}_{T1}.$$  \hspace{1cm} (2)

On the other hand, in the case of $State - T2$, differential values of electric charges in terminal-$I2$ and terminal-$O$ (see in Fig. 4), $\Delta q_{T2,V_o}$ and $\Delta q_{T2,V_i^o}$, are given by

$$\Delta q_{T2,V_o} = -\Delta q^{1}_{T2} = -\Delta q^{2}_{T2}$$

and

$$\Delta q_{T2,V_i^o} = \Delta q^{3}_{T2}.$$  \hspace{1cm} (3)

Here, averaged currents of terminal-$I2$ and terminal-$O$ are given by

$$\bar{I}_2 = (\Delta q_{T1,V_o} + \Delta q_{T2,V_o})/T = \Delta q_{V_o}/T$$

and

$$\bar{I}_o = (\Delta q_{T1,V_i^o} + \Delta q_{T2,V_i^o})/T = \Delta q_{V_i}/T,$$  \hspace{1cm} (4)

where $\Delta q_{V_o}$ and $\Delta q_{V_i}$ are electric charges in terminal-$I2$ and terminal-$O$, respectively. From Eqs. (1)–(4), the following equation is derived:

$$\bar{T}_2 = -3 \bar{I}_o.$$  \hspace{1cm} (5)

In Fig. 3, the energy consumed by resistors in one period, $W_T$, can be expressed by

$$W_T = W_{T1} + W_{T2},$$

where

$$W_{T1} = \frac{2R_{on}}{T_1}(\Delta q^{1}_{T1})^2 + \frac{2R_{on}}{T_1}(\Delta q^{2}_{T1})^2$$

and

$$W_{T2} = \frac{2R_{on}}{T_2}(\Delta q^{3}_{T1})^2 + \frac{2R_{on}}{T_2}(\Delta q^{3}_{T2})^2.$$
and \[ W_{T2} = \frac{3R_{\text{in}}}{T_2} (\Delta q_{T2})^2. \]

Here, it is known that a general equivalent circuit of SC power converters can be expressed by the determinant using a Kettenmatrix. In the general equivalent circuit [1], the consumed energy \( W_T \) is defined by

\[ W_T = W_{T1} + W_{T2} \equiv \left( \frac{\Delta v_{\text{in}}}{T} \right)^2 \cdot R_{SC} \cdot T, \tag{7} \]

where \( R_{SC} \) is called the SC resistance. By substituting Eqs. (2), (3), and (6) into (7), the SC resistance when the 3× step-up mode, \( R_{SC,3\times} \), is obtained by

\[ R_{SC,3\times} = \frac{4 - D}{D(1-D)} \cdot R_{\text{in}}, \tag{8} \]

where \( D (= T1/T) \) denotes the duty factor. Therefore, by using Eqs. (5) and (8), the equivalent circuit concerning the 3× conversion mode can be expressed by the circuit shown in Fig. 5. In Fig. 5, power efficiency \( \eta_{3\times} \) is obtained by

\[ \eta_{3\times} = \frac{P_{\text{out}}}{P_{\text{total}}} \quad \text{where} \]

\[ P_{\text{out}} = R_L \left( \frac{R_{\text{reg}}}{R_L + R_{\text{reg}}} \right)^2 \left( T_o \right)^2 \]

and \( P_{\text{total}} = P_{\text{out}} + 9R_{pe}(T_o)^2 + R_{SC,3\times}(T_o)^2 + R_{\text{reg}} \frac{R_L}{R_L + R_{\text{reg}}} \left( T_o \right)^2. \tag{9} \]

Especially, power efficiency \( \eta_{3\times} \) can be expressed by

\[ \eta_{3\times} = \frac{R_L}{R_L + 9R_{pe} + R_{SC,3\times}}. \tag{10} \]

To save space, only the characteristic of the 3× step-up mode is discussed in this manuscript. However, the characteristics of other conversion modes can also be analyzed by the same method. Table 1 shows the summary of the theoretical results concerning other conversion modes of the proposed converter.

\[ \begin{array}{|c|c|}
\hline
\text{Input range} & \text{Ratio} & R_{SC} \\
\hline
V_{\text{in}1}/2 \leq V_{\text{in}2} < 3V_{\text{in}1}/4 & 3\times & \frac{4 - D}{D(1-D)} \cdot R_{\text{in}} \\
3V_{\text{in}1}/4 \leq V_{\text{in}2} < V_{\text{in}1} & 2\times & \frac{1}{D(1-D)} \cdot R_{\text{in}} \\
V_{\text{in}1} \leq V_{\text{in}2} & 1.5\times & \frac{3 + D}{4D(1-D)} \cdot R_{\text{in}} \\
V_{\text{in}1}/2 > V_{\text{in}2} & 1.5\times & \frac{3 + D}{4D(1-D)} \cdot R_{\text{in}} \\
\hline
\end{array} \]

\[ ^1 \text{Of course, the consumed energy of peripheral circuits such as pulse generators, comparators, etc. is disregarded in the power efficiency.} \]

4. Simulation & Experiment

To verify the theoretical analyses, SPICE simulations were performed under conditions where \( N = 1, C_1-C_3 = 2 \mu F, D = 0.5, T = 1 \mu s \) and \( R_{\text{reg}} = 0.8 \Omega \). Figure 6 shows the power efficiency of the proposed converter as a function of output load \( R_L \), where \( R_{\text{reg}} \) was set to \( R_{\text{reg}} = \infty \). As Fig. 7 shows, theoretical results agree well with simulated results. To evaluate the power efficiency, the theoretical analysis was performed concerning conventional converter MAX1910 shown in Fig. 1. Table 2 shows the theoretical results of the conventional converter. As Tables 1 and 2 show, the power efficiency of the proposed converter is almost the same as that of the conventional converter, because the SC resistances are almost the same. However, unlike the conventional converter, the proposed converter can provide long battery runtime by converting clean energy input.

Next, to confirm the validity of circuit design, experiments were performed regarding to the proposed converter with \( N = 1 \). The experimental circuit was built with commercially available transistors on a bread board. Figure 7 shows the 3× stepped-up voltage obtained by the experimental circuit, where \( V_{\text{in}1} = 0 \text{ V}, V_{\text{in}2} = 4 \text{ V}, C_1-C_3 = 2.2 \mu F, D = 0.5, T = 1 \text{ ms} \) and \( R_L = 10 \text{ k} \Omega \). To save space, only the 3× stepped-up voltage is shown in this manuscript. Fig-

\[ \begin{array}{|c|}
\hline
\text{Output load (Ω)} & \text{Power efficiency (%)} \\
\hline
1 & 100 \% \\
10 & 95 \% \\
100 & 80 \% \\
1000 & 50 \% \\
\hline
\end{array} \]

\[ \text{Fig. 6 Power efficiency of proposed converter.} \]

\[ \begin{array}{|c|c|}
\hline
\text{Ratio} & R_{SC} \\
\hline
2\times & 3D \frac{D(1-D)}{D(1-D)} \cdot R_{\text{in}} \\
1.5\times & 3D \frac{D(1-D)}{D(1-D)} \cdot R_{\text{in}} \\
\hline
\end{array} \]

\[ ^1 \text{Of course, the consumed energy of peripheral circuits such as pulse generators, comparators, etc. is disregarded in the power efficiency.} \]
Fig. 7 3 × step-up mode obtained by experimental circuit.

Fig. 8 Charge process obtained by experiment.

Fig. 9 Experimental circuit to investigate charge process.

Fig. 8 shows the charge process obtained by the experiment\(^1\), where capacitor \(C_L = 100 \mu\text{F}\) was connected to terminal-\(V_{\text{in1}}\) in substitution for the lithium battery to reduce the experiment time. In the experiment of Fig. 8, to confirm the charge process, reset switch \(S_{\text{rst}}\) was attached to terminal-\(V_{\text{in1}}\) as shown in Fig. 9. As Fig. 8 shows, the proposed converter can charge the battery when the output load is light. Therefore, a long battery runtime will be realized by the proposed converter.

5. Conclusion

For the back-lighting application, a multiple-input switched-capacitor (SC) DC-DC converter realizing long battery-runtime has been proposed in this paper.

Concerning power efficiency, derived theoretical formulas will be helpful to estimate circuit characteristics, because theoretical results agreed well with SPICE simulation results. Furthermore, the proposed converter enables us to realize long battery runtime, because the battery charge process was confirmed through experiments.

References


\(^{1}\)When \(R_L = 10\, \text{k}\Omega\), the power efficiency of the experimental circuit was 48.6 %, where input power \(P_{\text{in}}\) was 3.7 mW and output power \(P_{\text{out}}\) was 1.8 mW. However, the power efficiency will be improved by integrating the proposed converter into an IC chip. Because, unlike an IC chip, the parasitic resistance of the bread board is very large. Therefore, only the circuit design was verified in this experiment.